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J.S. UTILITY Patent Applic	ation	
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER & I MCE LI-
09/505949		712	222	2783	,

Michael Chow Elango Ganesan John Phillips Nazar Zaidi

Method and apparatus for achieving architectural torrectness in a multi-mode processor providing floating-point support

PTO-2040 12/99

PATENT NUMBER

ISSUING CLASSIFICATION

CROSS REFERENCE(S)

CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

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TERMINAL	DRAWINGS			CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.0	
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subsequent to (date) has been disclaimed.	(Assistant Examiner)		(Date)			
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.	;			ISS	UE FEE,	
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uns patent have been disclaimed.	(Legal Instrum	ents Examiner)	(Date)			
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